

### **Abstract of the Disclosure**

5 A Verilog/PLI module that simulates behavior of an electronic system is co-simulated in a clock-accurate manner with a System C module that models the behavior of a component of the electronic system using a remote procedure call (RPC). Adding RPC functionality to the Verilog/PLI and System C modules facilitates exchanging parameters between the modules. The RPC is used to transfer process control to the SystemC module, after execution of which control is returned to the Verilog/PLI module. A return value is also returned to the Verilog/PLI module; this return value represents output signals associated with the System C module.

"Express Mail" mailing label number: EL873860705US

Date of Deposit: August 28, 2001

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to the Commissioner for Patents, Box Patent Application, Washington, D.C. 20231.